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Anthony P. Curtis, Ph.D., Reg. No. 46,193

Name of applicant, assignee, or Registered Representative

7/8/05  
Signature

Date of Signature

Certificate  
JUL 18 2005  
of Correction

Our File No. 9281-4614

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Takeo Suzuki et al.

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Patent No.: 6,828,844 B2

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Examiner: My-Trang Nuton

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Issue Date: December 7, 2004

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Group Art Unit: 2816

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Serial No.: 10/628,213

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Filing Date: July 28, 2003

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For: MIXER WHICH ASSURES

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AT LOW SUPPLY VOLTAGE

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**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

Please issue a Certificate of Correction for the above-identified patent to correct the errors listed on the accompanying PTO Form 1050. This request is being made pursuant to 35 U.S.C. § 254, since the errors comprise minor or typographical errors by the Patent and Trademark Office.

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,



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Anthony P. Curtis, Ph.D.  
Registration No. 46,193  
Agent for Applicants

BRINKS HOFER GILSON & LIONE  
P.O. Box 10395  
Chicago, Illinois 60610  
(312) 321-4200

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 6,828,844 B2

DATED : December 7, 2004

Page 1 of 1

INVENTOR(S) : Takeo Suzuki et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims

In claim 1, line 8, immediately after "second transistor" insert --,-- (comma).

MAILING ADDRESS OF SENDER:

Anthony P. Curtis, Ph.D.  
BRINKS HOFER GILSON & LIONE  
P.O. Box 10395  
Chicago, Illinois 60610

PATENT NO. 6,828,844 B2

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